

CSCE 212: Introduction to Computer Architecture

Meeting times: T TH 10:05-11:20

Locations: Swearingen 2A31

Textbook: *Computer Organization and Design: The Hardware/Software Interface 5/e*
by John Hennessy and David Patterson
Morgan Kaufmann (Elsevier) Publishers

Prerequisites: CSCE 211 (Digital Logic Design) and
[CSCE 145 (Alg. Des. I) or CSCE 206 (Sci. Appl. Prog.)]

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Office hours: by appointment

Course Organization:

Chapter 1:	Introduction/Performance	1 week
Chapter 2:	MIPS Instruction Set Architecture	4 weeks
Chapter 3:	Computer Arithmetic	4 weeks
Chapter 4:	Datapath	4 weeks
Chapter 5:	Memory Hierarchy	2 weeks

Grading Structure:

Quizzes (7-8)	30%
Projects (3)	30%
Exams (3)	40%

Grading Scale:

A	$90 \leq \text{score} \leq 100\%$
B+	$85 \leq \text{score} < 90\%$
B	$80 \leq \text{score} < 85\%$
C+	$75 \leq \text{score} < 80\%$
C	$70 \leq \text{score} < 75\%$
D+	$65 \leq \text{score} < 70\%$
D	$60 \leq \text{score} < 65\%$
F	$\text{score} < 60$

Web-Based Resources

Projects are submitted and grades disseminated via the **CSE "Moodle" Dropbox** (<http://dropbox.cse.sc.edu>)

Policy on Late Submissions

Late projects will be charged a 10% grade penalty for each school day after the due date, but limited to a maximum of 30%.

At most, you may only submit **one version** of any one project.

Academic Honesty Policy

Students are encouraged to assist their colleagues for the purpose of overcoming technical challenges related to the use of the programming tools (i.e. MARS). However, any collaboration beyond this is prohibited and subject to the university's guidelines, regulations, and policies regarding academic dishonesty.

Students with Disabilities

Any student with a documented disability should contact the Office of Student Disability Services at 777.6142 to make arrangements for appropriate accommodations.

Syllabus Change Policy

This syllabus is a guideline for the course and is subject to change with advance notice.