



## CSCE 611: Digital Systems Design I

### Homework/Lab/Project - Cover Sheet

**PROJECT:** \_\_\_\_\_ **DATE:** \_\_\_\_\_

<b>Team Members</b>

<b>Parameter</b>	<b>Design-1</b>	<b>Design-2</b>	<b>Design-3</b>
<i>Estimated Area (CLBs)</i>			
<i>Worst-case Delay (ns)</i>			
<i>Max clock frequency (MHz)</i>			
<i>Estimated Power dissipation (mw)</i>			

<b>Effort Distribution Report</b>	<i>Team Member 1</i>			<i>Team Member 2</i>		
<b>Effort Tasks (hours)</b>	<b>Design-1</b>	<b>Design-2</b>	<b>Design-3</b>	<b>Design-1</b>	<b>Design-2</b>	<b>Design-3</b>
<i>System Definition and Partitioning</i>						
<i>Graphical Entry</i>						
<i>Simulation (including test design, threads, etc.)</i>						
<i>Logic Synthesis</i>						
<i>Place &amp; Route</i>						