



## CSCE 491: Capstone Systems Design Project

### Homework/Lab/Project # \_\_\_\_\_ - Cover Sheet

PROJECT: \_\_\_\_\_ DATE: \_\_\_\_\_

Team Members
1.
2.

Parameter	Design-1	Design-2	Design-3
<i>Estimated Area (<math>\mu\text{m}^2</math>)</i>			
<i>Worst-case Delay (ns)</i>			
<i>Max clock frequency (MHz)</i>			
<i>Estimated Power dissipation (nw)</i>			

Effort Distribution Report	<i>Team Member 1</i>			<i>Team Member 2</i>		
Effort Tasks (hours)	Design-1	Design-2	Design-3	Design-1	Design-2	Design-3
<i>System Definition and Partitioning</i>						
<i>Graphical Entry</i>						
<i>Simulation (including test design, threads, etc.)</i>						
<i>Logic Synthesis</i>						
<i>Place and Route</i>						