

## Setting up your environment

The first thing you need to do before getting started is to set up your Linux account for the design tools.

Log into your Linux account and open a terminal.



Then type:

```
gedit .profile
```

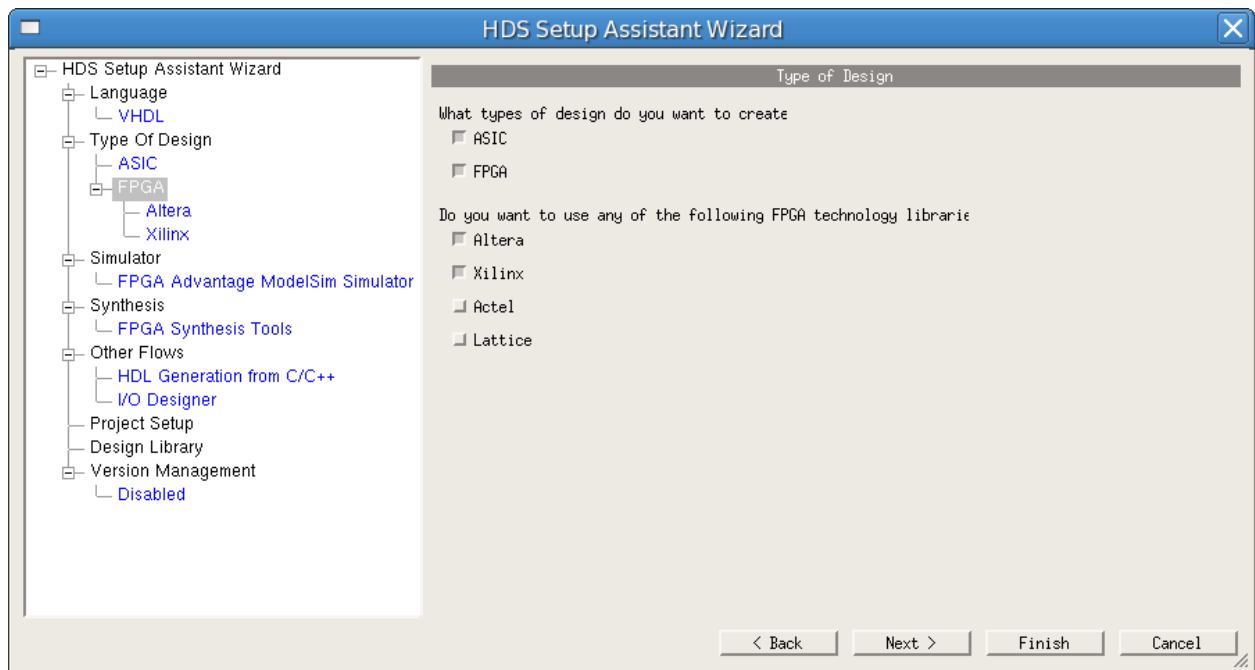
In the text editor, add the following lines to the end of the file.

```
source /usr/local/3rdparty/cad_setup_files/mentor.bash
```

```
source /usr/local/3rdparty/cad_setup_files/altera.bash
```

Save the file and close the editor. You will only need to do this ONCE. Now log out and log back into your workstation.

Invoke FPGA Advantage using the command “**hdl\_designer &**”. If this is the first time you invoke HDL Designer, the HDS Setup Assistant wizard is shown.



Cancel the wizard. HDS starts with the default examples project, VHDL as the default language and all the available flows and tools.