

CSCE 212: Introduction to Computer Architecture

Meeting times: TTH 11:00-12:15 (section 001)
TTH 8:00-9:15 (section 002)

Locations: Swearingen 2A27

Textbook: *Computer Organization and Design: The Hardware/Software Interface*
by John Hennessy and David Patterson
Morgan Kaufmann (Elsevier) Publishers

Prerequisites: CSCE 211 (Digital Logic Design) and
[CSCE 145 (Alg. Des. I) or CSCE 206 (Sci. Appl. Prog.)]

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Office hours: TTH 10:00 – 11:00

Course organization:

Topic	Lectures
MIPS Instruction Set Architecture	4 weeks
Performance	1 week
Exam 1 -- before 2/23 (2/19)	
Computer Arithmetic	3 weeks
Processor Design / Micro-architecture	3 weeks
Memory	1 week
Storage, Networks, and Peripherals	1 week
Multiprocessing	1 week

Final Exam

Grading structure:

Chapter quizzes	30%
Projects	30%
Exams	40%

Grading scale:

$90 \leq \text{cumulative score} \leq 100$	->	A
$80 \leq \text{cumulative score} < 90$	->	B
$70 \leq \text{cumulative score} < 80$	->	C
$60 \leq \text{cumulative score} < 70$	->	D

Projects will be submitted via **CSE Dropbox**. Grades will be disseminated via **USC Blackboard**.

Policy on late submissions

Late homework and projects will be charged a 10% grade penalty for each weekday after the due date.

Academic honesty policy

Students are encouraged to assist their colleagues for the purpose of overcoming technical challenges related to the use of the programming tools (i.e. SPIM). However, any collaboration beyond these exceptions is prohibited and is subject to the university's guidelines, regulations, and policies regarding academic dishonesty.