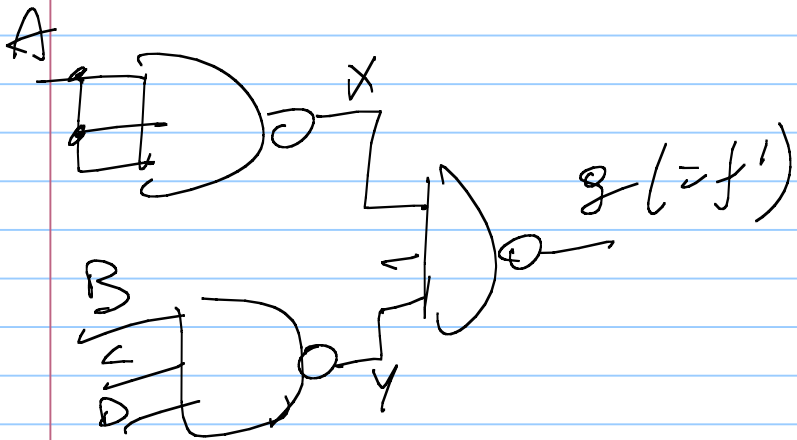
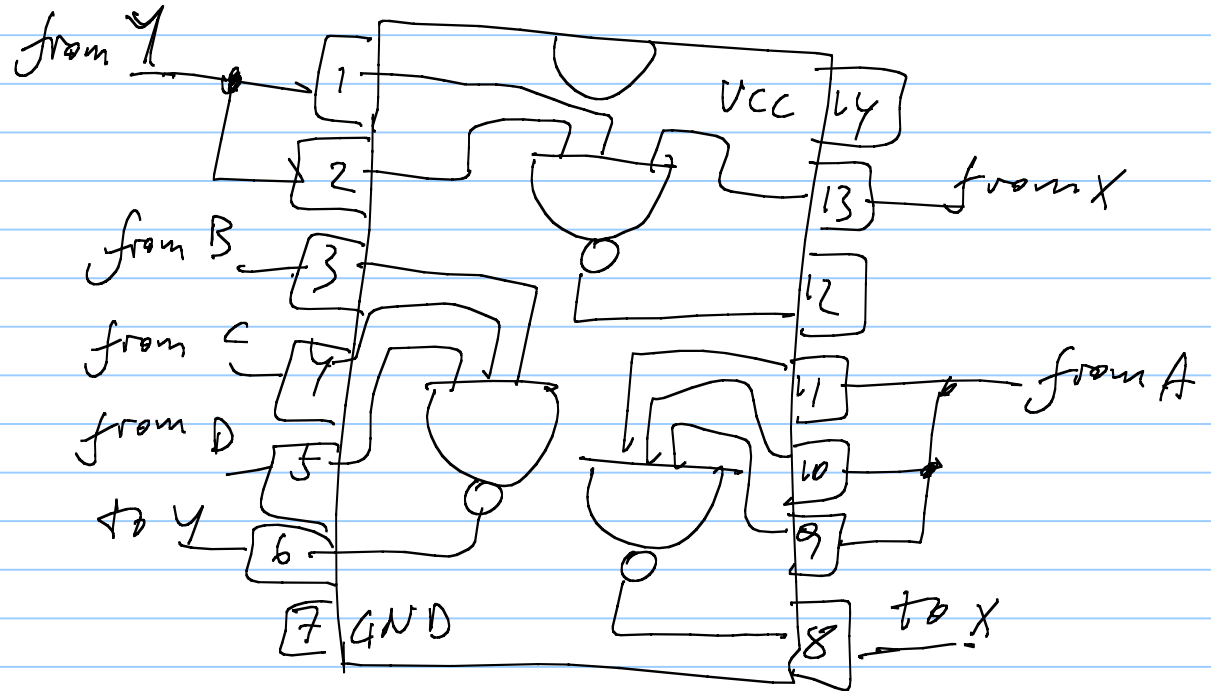
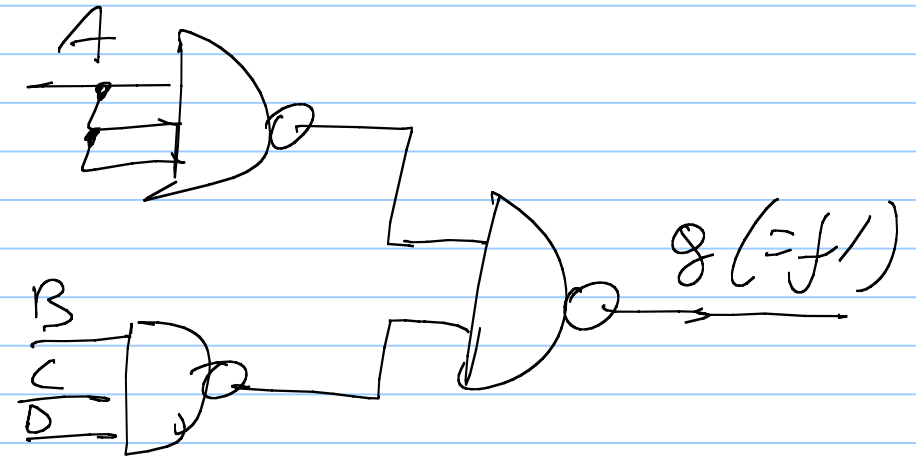


Circuit 2



Mr. Warren
Mr. Evans

	A	B	C	D	f	g=f'
m ₀	0	0	0	0	1	0
m ₁	0	0	0	1	1	0
m ₂	0	0	1	0	1	0
m ₃	0	0	1	1	1	0
m ₄	0	1	0	0	1	0
m ₅	0	1	0	1	1	0
m ₆	0	1	1	0	1	0
m ₇	0	1	1	1	0	1
m ₈	1	0	0	0	0	1
m ₉	1	0	0	1	0	1
m ₁₀	1	0	1	0	0	1
m ₁₁	1	0	1	1	0	1
m ₁₂	1	1	0	0	0	1
m ₁₃	1	1	0	1	0	1
m ₁₄	1	1	1	0	0	1
m ₁₅	1	1	1	1	0	1



Three NAND gates
from the 7410 chip.
which has three NAND gates